



# KTU SPONSORED FACULTY DEVELOPMENT PROGRAMME ON

## SPONSORSHIP

Certified that .....  
Department of .....  
of .....  
..... is hereby permitted to  
attend the FDP on "Emerging Trends & Challenges  
in Low Power VLSI Design" at Mar Baselios  
Christian College of Engineering & Technology,  
Peermade from 19/05/2021 to 21/05/2021.

DATE:  
PLACE:

Signature and Seal of the Head  
of the Institution

## REGISTRATION DETAILS

**How to Apply:** Register your participation by  
submitting the google form given below on or before  
15/05/2021

Registration Link:

<http://forms.gle/pk7VGSUfev9Z0q378>

## IMPORTANT DATES

Commencement of Registration: **28/04/2021**

Last Date of Registration: **15/05/2021**

Confirmation of Participants via E-mail: **16/05/2021**

Commencement of Programme: **19/05/2021**

SCAN & REGISTER:

FDP PLATFORM:



## CHIEF PATRON

His Holiness Baselios Marthoma Paulose II  
*President*

## PATRONS

Rev. Fr. Gigi P. Abraham, Director  
Dr. Jayaraj Kochupillai, Principal  
Mr. K. A. Abraham, Bursar

## CONVENER

Prof. Elias Janson K, HoD  
*Department of Electronics & Communication Engineering*

## ORGANIZING SECRETARIES

Mr. Vishnu V. Gopi, Assistant Professor  
Mrs. Anu Mary Mathew, Assistant Professor

## EVENT CO-ORDINATORS

Mr. Amol Joy, Assistant Professor  
Mr. Lijosh Mathews, Assistant Professor  
Mr. Tom J. Kuriakose, Assistant Professor  
Ms. Neethu Susan Rajan, Assistant Professor  
Ms. Maria Joseph, Assistant Professor  
Mrs. Almaria Joseph, Assistant Professor  
Mr. Geethos Ninan, Assistant Professor

## TECHNICAL CO-ORDINATORS

Mrs. Julimol Joy, Lab Instructor  
Mr. Bobby George, Senior Lab Assistant  
Mr. Devajan A, Lab Assistant

## EMERGING TRENDS & CHALLENGES IN LOW-POWER VLSI DESIGN

Organized by  
**DEPARTMENT OF  
ELECTRONICS & COMMUNICATION  
ENGINEERING**



**MAR BASELIOS CHRISTIAN**  
COLLEGE OF ENGINEERING & TECHNOLOGY  
KUTTIKANAM, PEERMADE



**MAY  
19,20,21**

## CONTACT US

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## ABOUT THE INSTITUTION

Mar Baselios Christian College of Engineering & Technology, Kuttikkanam, Peermade, is a Self-Financing Institution for Professional Education, affiliated to APJ Abdul Kalam Technological University (KTU), Trivandrum and approved by All India Council for Technical Education (AICTE), New Delhi. The College is owned and managed by the Malankara Orthodox Syrian Church, which plays a paternal role in the Institutions establishment and operations. The Institution focuses on offering baccalaureate degree programs in various Engineering streams. In 2001, the College started functioning as a new generation Engineering College. Since its inception in the year 2001, the College has been on a steady path of growth and up-gradation. Our aim is to provide our students with the best possible facilities and the right training. We emphasize on teaching excellence and wholesome learning experience and strive towards making MBC a premier Educational Institution.

## ABOUT THE DEPARTMENT

The Electronics and Communication Engineering department was started in the year 2001. The department offers B.Tech, M.Tech and Ph.D programs that provide students with the knowledge and tools they need to succeed in the Electronics and Communication Engineering. Electronics Engineering deals with implementation applications, principles and algorithms developed among many related fields, for example Solid-State Physics, Radio Engineering, Telecommunications, Control Systems, Signal Processing Systems, Computer Engineering, Instrumentation Engineering, Electric Power Control, Robotics and many others. The department is now meeting the requirement of technological challenges & the technical man power requirement through its updated course curriculum, experienced faculty members, skilled supporting staffs and with the help of sophisticated laboratories.

## VISION

- ▶ Molding Electronics Engineers with adaptability to changing technological world with professional competence to benefit society.

## MISSION

- ▶ Provide theoretical and practical knowledge through quality education.
- ▶ To establish state-of-the art laboratories to facilitate research and innovation.
- ▶ To continuously upgrade the knowledge and skills to cop up with latest advancements.
- ▶ To instill effective communication skills, ethical practices and professional skills to work in a collaborative environment.

## ABOUT FDP

In the present scenario, Very Large Scale Integration (VLSI) is considered to be one of the fastest growing leading edge technologies. Each and every gadget which we use in day-to-day life involves VLSI directly or indirectly. Advances in VLSI technology and its trend towards the low leakage high performance applications makes the academicians and industrialists to work in the field of VLSI design. With the development of Digital Signal Processors and High Performance Computing, VLSI based design has become the technology of choice. Requirement of miniaturization of the devices lead to more and more compact and efficient design structure. The goal of low power design is to reduce the individual components of power as much as possible, thereby reducing the overall power consumption. This FDP will give the participants a valuable resource for enhancing their knowledge about the current trends and challenges in Low Power VLSI Design.

## OBJECTIVES

The objective of this FDP is to provide a better exposure to the faculty members & research scholars in the field of Low Power VLSI Design and to provide research aspects and solutions to the various problems related to the VLSI Design.

## RESOURCE PERSONS

- ▶ Dr. Tripti S. Warriar, Assistant Professor  
Department of Electronics  
Cochin University of Science & Technology
- ▶ Dr. Nalesh S, Assistant Professor  
Department of Electronics  
Cochin University of Science & Technology
- ▶ Mr. Sudheesh Madhavan, Director  
Tecnode Solutions Private Limited, Bangalore
- ▶ Mr. Jagadeesh Kumar P, Assistant Professor  
Model Engineering College, Thrikkakara
- ▶ Dr. Rekha K James, Professor  
School of Engineering, Cochin University of Science & Technology
- ▶ Dr. Jobymol Jacob, Professor & Principal in Charge  
College of Engineering. Poonjar
- ▶ Mr. Arun A V, Research Scholar  
Model Engineering College, Thrikkakara

## COURSE CONTENT

- ▶ Introduction to VLSI Technology, Recent Trends in VLSI
- ▶ Analytical modelling of MOS Devices in VLSI, Power Dissipation in MOSFET Devices.
- ▶ CMOS Technology, Power Dissipation in CMOS
- ▶ VLSI Design and FOSS EDA Tools
- ▶ Design of Low Power VLSI circuits
- ▶ Emerging Low Power Devices

## TARGET AUDIENCE

The FDP is open to faculty members from all the Engineering Colleges affiliated to APJ Abdul Kalam Technological University, research scholars and PG Students.