

Report on Workshop “SPICE simulation, PCB Design, VHDL and Arduino”

Organized by the Department of
Electronics and Communication
Engineering in association with AZTECS

Date and Venue

The Workshop took place from July 25 to July 29, 2016 at Mar Baselios Christian College of Engineering and Technology, Peermade

Resource Person

Experts from Visionics India Pvt Ltd, Technopark campus Thiruvanthapuram

Participants

- BTech Third year Students

Objective of the Workshop

The main objectives of the workshop are:

- Introduction to SPICE simulation.
- Development of layout using PCB design
- To understand the basics of VHDL and Arduino.

Duration of the Workshop

- July 25 to July 29, 2016

Certification and Awards:

- Certification of Participation to all

Workshop Contents

Session 1

An Introduction about SPICE and PCB design

- Simulation
- Development of circuit layout using SPICE

Session 2

An introduction about VHDL and Arduino

- VHDL Basics
- Development of models using VHDL
- Development of project models using Arduino.