

**Faculty Development Programme on**  
**“ELECTRIC CIRCUIT ANALYSIS”**

**Organized by the Department of Electronics and Communication Engineering**

**Date and Venue**

FDP on Electric Circuit Analysis was held at Mar Baselios Christian College of Engineering and Technology, Peermade from 10-07-2018 to 13-07-2018.

**Resource Person**

Prof. Dr. K.P Zachariah

Professor, Department of Electronics and Communication Engineering, SAINTGITS College of Engineering, Pathamuttom, Kottayam.

**Participants**

- Faculty members from ECE and EEE department of MBCCET, Peermade and other Engineering colleges.

**Objective of the Workshop**

The main objectives of the workshop are:

- Introduction to circuit analysis.
- Bartlett's bisection theorem and applications.
- Miller's Theorem and applications.
- Obtaining circuit models of active devices.
- Two - port network representations - Mathematical and circuit models.
- Logic behind techniques used in steady state analysis of ac circuits.
- Good teaching.
- Average power calculations.

**Duration of the Workshop**

- Four Full Day (6 hours daily).

**Certification and Awards**

- Certification of participation to all.

Prepared by,

Anu Mary Mathew (FDP Coordinator)

## PHOTOGRAPHS OF FDP

