| Course No. | Course Name | L-T-P - Credits | Year of Introduction |
|--|---|----------------------------|-------------------------|
| EE231 | ELECTRONIC CIRCUITS LAB | 0-0-3-1 | 2016 |
| Course Object | ives | | _010 |
| To design and | develop various electronic circuits using di | screte components and | OPAMPs. |
| List of Exercis | es/Experiments : (Out of 18 experiments | listed, 12 experiments | are mandatory. |
| 1.Study & Use | of CRO: Measurement of current voltage, | frequency and phase sl | nift. |
| 2.Half wave an | d Full wave (Centre-tapped and bridge) Re | ectifiers with and without | ut filters- |
| Calculation of | Ripple factor, Rectification efficiency, and | % regulation. | |
| 3. Clipping circ | cuits using diodes | CICAL | |
| 4. Clamping cir | cuits using diodes | (ILAL | |
| 5. RC coupled | amplifier using BJT in CE configuration-1 | Measurement of gain, in | put and |
| output impedar | ce and frequency response | Y | - |
| 6. JFET amplif | ier- Measurement of voltage gain, current | gain, input and output in | npedance |
| 7.Design and te | esting of simple zener voltage regulators | | |
| 8.OPAMP circ | uits – Design and set up of inverting and ne | on-inverting amplifier, s | scale changer, |
| adder, integrate | or, differentiator | | |
| 9. Precision rec | tifier using Op-amps | | |
| 10.Phase shift of | oscillator using OPAMPs. | | |
| 11.Wein's Brid | ge oscillator using OPAMPs. | | |
| 12.Waveform g | generation – Square, triangular and sawtoo | th wave form generation | n using |
| OPAMPs. | | | |
| 13. Basic comp | arator and schmitt trigger circuits using O | p-amp | |
| 14. Design and | l testing of series voltage regulator using z | ener diode | |
| 15. Astable and | monostable circuit using 555 IC | | |
| 16. RC phase s | hift oscillator using BJT | | |
| 17.Introduction | to circuit simulation using any circuit sim | ulation software. | |
| 18. Introduction | n to PCB layout software | | |
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| Expected out | come. | 1 | · DIT 1 |
| The student si | louid be able to design and implement vari | ous electronic circuits t | ising BJTs and |
| OPAMPS. | Lotu. | | |
| Toy t D | alt/Defenences | | |
| 1 Molvin | DOR/References: | 7/2 Tata MaCrow Hill | 2010 |
| 2 Roylested P. J. and L. Nashelsky, Electronic Dovices and Circuit Theory 10/2 Decrean | | | |
| 2. Doylest | on India 2009 | ices and Cheunt Theory | , 10/0, 1 Carsoll |
| 2 Choudh | ury R. Linear Integrated Circuits New A. | Te International Dublich | ers 2008 |
| J. Millman | n L and C C Halkias Integrated Electron | cs: Analog and Digital | Circuits and |
| 4. IVIIIIIIa | Tata McGraw-Hill 2010 | co. Analog and Digital | Circuits allu |
| Systems | 5, 1 a a W O a w - 1111, 2010. | | |

Systems, Tata McGraw-Hill, 2010.